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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	INTEGRATED CIRCUIT SELECTIVE SCALING
Application Number	1
Date :	
First Named Applica	ant: Fook-Luen Heng
Confirmation Number	er:
Attorney Docket Nu	mber: BUR920040201US1

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Documents being submitted:	Files
us-assignment	BUR920040201US1-usassn.xml
do doorgrimont	us-assignment.xsl
	us-assignment.dtd
	BUR920040201US1Assign1of5.tif
	BUR920040201US1Assign2of5.tif
	BUR920040201US1Assign3of5.tif
	BUR920040201US1Assign4of5.tif
	BUR920040201US1Assign5of5.tif
us-request	BUR920040201US1-usrequ.xml
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	us-request.xsl
us-fee-sheet	BUR920040201US1-usfees.xml
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	us-fee-sheet.dtd
us-ids	BUR920040201US1-usidst.xml
46 146	us-ids.dtd
	us-ids.xsl
us-declaration	BUR920040201US1Dec1of5.tif
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us-declaration	BUR920040201US1Dec5of5.tif
application-body	BUR920040201US1ABX.xml
application body	application-body.dtd
	Image1.tif
	Image2.tif
	Image3.tif
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	isomscr.ent
	isonum.ent
	isopub.ent
	isotech.ent
	mathml2.dtd

	mathml2-qname-1.mod
	mmlalias.ent
	mmlextra.ent
	soextblx.dtd
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	wipo.ent
application-body-pdf-wrap	BUR920040201US1ABX-pdf-wrap.xml
abstract-pdf	BUR920040201US1ABX-abst.pdf
claims-pdf	BUR920040201US1ABX-clms.pdf
description-pdf	BUR920040201US1ABX-desc.pdf
drawings-pdf	BUR920040201US1ABX-draw.pdf
us-declaration	BUR920040201US1Dec3of5.tif